

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of

Applicants : Kelly T. Hurley et al

Serial No. : 09/808,484

Confirm. No. : 3606

Filed : March 14, 2001

Title : **SELF-ALIGNED FLOATING GATE FLASH CELL SYSTEM AND METHOD**

Docket : MIO 0064 PA

Examiner : Quinto, Kevin V.

Art Unit : 2826

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office (Fax. No. 703/872-9318) on January 7, 2002.


William A. Jividen - Attorney

Reg. No. 42,695

Assistant Commissioner for Patents
Washington, D.C. 20231

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JAN 7 2002

Sir:

RESPONSE TO RESTRICTION REQUIREMENT

TECHNOLOGY CENTER 2800

This paper is being filed in response to the Office Action of December 14, 2001.

Submitted herewith, Applicants preliminary amend the pending application, submitting new claims 73-90. Applicants submit that new claims 73-90 read generically on the embodiments of FIGS. 1b, 2, 3, 4, 6, and 8. However, if necessary, Applicants hereby provisionally elect claims 9-11, 12-16, 19-22, 24, 25, 33-38, 40, 53-57 and 73-90 for prosecution on the merits as reading generically on the embodiments of FIGS. 2, 3, 6 and 8.

Applicants respectfully submit that the application is in condition for allowance. The Examiner is encouraged to contact the undersigned to resolve efficiently any formal matters or to discuss any aspects of the application or of this response. Otherwise, early notification of allowable subject matter is respectfully solicited.

Respectfully submitted,

KILLWORTH, GOTTMAN, HAGAN &
SCHAEFF, L.L.P.By William A. Jividen
Registration No. 42,695

One Dayton Centre
One South Main Street, Suite 500
Dayton, Ohio 45402-2023
Telephone: (937) 223-2050
Facsimile: (937) 223-0724
WAJ/

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PRELIMINARY AMENDMENT

Please preliminarily amend the above-identified application as follows.

In the Claims

Please insert the following new claims:

73. A stacked gate region of a memory cell comprising:
a field oxide region;
a tunnel oxide layer;
at least one floating gate layer formed over said tunnel oxide layer; and
at least one polysilicon structure formed adjacent to said at least one floating gate layer,
said polysilicon structure is adapted to increase capacitive coupling of said memory cell.
74. The stacked gate region of claim 73 wherein said polysilicon structure is selected from the group consisting of at least one wing and at least one ear.
75. The stacked gate region of claim 74 wherein said polysilicon structure is at least one wing, said wing being formed over a portion of said field oxide region.

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